-	4530	c$2 and $2hdl	USPAT	2003/10/16 15:29
-	442	(c$2 and $2hdl) and fpga	USPAT	2003/10/16 15:29
-	429	(c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable))	USPAT	2003/10/16 15:31
-	223	(c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable)) and compile$2	USPAT	2003/10/16 15:31
-	131	((c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable)) and compile$2) and 716/$.ccls.	USPAT	2003/10/17 06:26
-	131	((c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable)) and compile$2) and 716/$.ccls.	USPAT	2003/10/17 06:55
-	2233	algorithm near3 hardware	USPAT	2003/10/17 06:55
-	279	algorithm near3 hardware and (circuit near3 design)	USPAT	2003/10/17 06:55
-	60	(algorithm near3 hardware and (circuit near3 design)) and 716/$.ccls.	USPAT	2003/10/17 06:56
-	44	((algorithm near3 hardware and (circuit near3 design)) and 716/$.ccls.) not (((c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable)) and compile$2) and 716/$.ccls.)	USPAT	2003/10/17 07:01
-	115	(((c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable)) and compile$2) and 716/$.ccls.) not ((((algorithm near3 hardware and (circuit near3 design)) and 716/$.ccls.) not (((algorithm near3 hardware and (circuit near3 design)) and 716/$.ccls.) not (((c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable)) and compile$2) and 716/$.ccls.)))	USPAT	2003/10/17 07:01
-	16	((algorithm near3 hardware and (circuit near3 design)) and 716/$.ccls.) not (((algorithm near3 hardware and (circuit near3 design)) and 716/$.ccls.) not (((c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable)) and compile$2) and 716/$.ccls.))	USPAT	2003/10/17 07:36
-	17	6226776.uref.	USPAT	2003/10/17 07:51
-	448	((convert$3 or translat$4) with code with hardware)	USPAT	2003/10/17 07:52
-	0	(((convert$3 or translat$4) with code with hardware)) and 716/19.ccls.	USPAT	2003/10/17 07:52
-	24	(((convert$3 or translat$4) with code with hardware)) and 716/$.ccls.	USPAT	2003/10/17 08:20
-	33	5603043.uref.	USPAT	2003/10/17 08:20
-	31	5603043.uref. not ((((convert$3 or translat$4) with code with hardware)) and 716/$.ccls.)	USPAT	2003/10/17 08:38
-	378539	bit ajd serial or digital adj2 serial or fully adj2 parallel	USPAT	2003/10/17 10:54
-	9669	bit adj serial or digital adj2 serial or fully adj2 parallel	USPAT	2003/10/17 10:54
-	57	(bit adj serial or digital adj2 serial or fully adj2 parallel) and 716/$.ccls.	USPAT	2003/10/17 11:03
-	1	("6545501").PN.	USPAT	2003/10/17 12:26

L Number	Hits	Search Text	DB	Time stamp
1	131	((c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable)) and compile$2) and 716/$.ccls.	USPAT	2003/10/17 06:55
2	2233	algorithm near3 hardware	USPAT	2003/10/17 06:55
3	279	algorithm near3 hardware and (circuit near3 design)	USPAT	2003/10/17 06:55
4	60	(algorithm near3 hardware and (circuit near3 design)) and 716/$.ccls.	USPAT	2003/10/17 06:56
5	44	((algorithm near3 hardware and (circuit near3 design)) and 716/$.ccls.) not (((c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable)) and compile$2) and 716/$.ccls.)	USPAT	2003/10/17 07:01
7	115	(((c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable)) and compile$2) and 716/$.ccls.) not (((algorithm near3 hardware and (circuit near3 design)) and 716/$.ccls.) not (((algorithm near3 hardware and (circuit near3 design)) and 716/$.ccls.) not (((c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable)) and compile$2) and 716/$.ccls.)))	USPAT	2003/10/17 07:01
6	16	((algorithm near3 hardware and (circuit near3 design)) and 716/$.ccls.) not (((algorithm near3 hardware and (circuit near3 design)) and 716/$.ccls.) not (((c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable)) and compile$2) and 716/$.ccls.))	USPAT	2003/10/17 07:36
8	17	6226776.uref.	USPAT	2003/10/17 07:51
9	448	((convert$3 or translat$4) with code with hardware)	USPAT	2003/10/17 07:52
10	0	(((convert$3 or translat$4) with code with hardware)) and 716/19.ccls.	USPAT	2003/10/17 07:52
11	24	(((convert$3 or translat$4) with code with hardware)) and 716/$.ccls.	USPAT	2003/10/17 08:20
12	33	5603043.uref.	USPAT	2003/10/17 08:20
13	31	5603043.uref. not (((((convert$3 or translat$4) with code with hardware)) and 716/$.ccls.)	USPAT	2003/10/17 08:38
-	663	((716/9) or (716/10) or (716/12)).CCLS.	USPAT	2003/03/10 14:08
-	428	(716/2).CCLS.	USPAT	2003/03/10 14:08
-	16	((716/8).CCLS.) and (electromagnetic or emi or emc)	USPAT	2003/03/10 14:20
-	3787	((electromagnetic near2 interference) or emi or emc) and (emmission or emission)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/10 14:24
-	1689	((electromagnetic near2 interference) or emi or emc) same (emmission or emission)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/10 14:24
-	0	((electromagnetic near2 interference) or emi or emc) same ((emmission or emission) and suseptibility)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/10 14:25
-	1349	((electromagnetic near2 interference) or emi or emc) with (emmission or emission)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/10 14:25
-	8	(((electromagnetic near2 interference) or emi or emc) with (emmission or emission)) and 716/$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/10 14:29

-	343	emc with layout or floorplan$3	USPAT	2003/03/10 14:30
-	11	emc with (layout or floorplan$3)	USPAT	2003/03/10 14:31
-	73	(emc or emi or (electromagnetic adj2 interference)) with (layout or floorplan$3)	USPAT	2003/03/10 15:22
-	0	(emc and (layout or floorplan$4)).ti.	USPAT	2003/03/10 15:23
-	1	(emc and (layout or floorplan$4)).ab.	USPAT	2003/03/10 15:24
-	17	(emc same(layout or floorplan$4))	USPAT	2003/03/10 15:24
-	17	(emc same (layout or floorplan$4))	USPAT	2003/03/10 15:26
-	399	(716/8).CCLS.	USPAT	2003/03/10 15:43
-	4	(calculat$4 with placement) and emc	USPAT	2003/03/12 09:00
-	4	emc with placement	USPAT	2003/03/12 09:01
-	2622	emc sameplacement	USPAT	2003/03/12 09:01
-	9	emc same placement	USPAT	2003/03/12 09:02
-	7	716/$.ccls. and emc	USPAT	2003/03/12 09:07
-	881	calculat$3 with layout	USPAT	2003/03/12 09:08
-	214	(calculat$3 with layout) and 716/$.ccls.	USPAT	2003/03/12 09:08
-	54	(calculat$3 with layout) and electromagnetic	USPAT	2003/03/12 09:08
-	8	((calculat$3 with layout) and electromagnetic) and 716/$.ccls.	USPAT	2003/03/12 09:10
-	3492	circuit adj2 description	USPAT	2003/03/12 09:10
-	13	(circuit adj2 description) with placement	USPAT	2003/03/12 09:23
-	10	(hdl or description) same (calculate with layout)	USPAT	2003/03/12 09:25
-	217	(calculat$3 with layout) and 716/$.ccls.	USPAT	2003/03/12 09:27
-	2636	emc or emi and 716/$.ccls.	USPAT	2003/03/12 09:27
-	21	(emc or emi) and 716/$.ccls.	USPAT	2003/03/12 09:33
-	401	(716/8).CCLS.	USPAT	2003/03/12 09:42
-	63	hdl with layout	USPAT	2003/03/12 09:54
-	51	(hdl or netlist) with floorplan$4	USPAT	2003/03/12 10:01
-	1433	(716/2,8,9,10,12).CCLS.	USPAT	2003/10/16 14:19
-	809	(703/13,14).CCLS.	USPAT	2003/10/16 14:19
-	2180	((716/2,8,9,10,12).CCLS.) ((703/13,14).CCLS.)	USPAT	2003/10/16 14:19
-	29	(((716/2,8,9,10,12).CCLS.) ((703/13,14).CCLS.)) and (emi or electromagnetic near2 field)	USPAT	2003/10/16 14:20
-	33	(((716/2,8,9,10,12).CCLS.) ((703/13,14).CCLS.)) and (emi or electromagnetic near2 field)	USPAT	2003/10/16 14:21
-	0	hewlett$.as. and whitmore-s.xa.	USPAT	2003/10/16 15:20
-	4	hewlett$.as. and whitmore-s$.xa.	USPAT	2003/10/16 15:23
-	4530	c$2 and $2hdl	USPAT	2003/10/16 15:29
-	442	(c$2 and $2hdl) and fpga	USPAT	2003/10/16 15:29
-	429	(c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable))	USPAT	2003/10/16 15:31
-	223	(c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable)) and compile$2	USPAT	2003/10/16 15:31
-	131	((c$2 and $2hdl) and (fpga and (custom or customized or dynamic or dynamically or reconfigurable or programmable or customizable)) and compile$2) and 716/$.ccls.	USPAT	2003/10/17 06:26

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L Number	Hits	Search Text	DB	Time stamp
-	1151	yoshikawa-a$10.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/21 10:02
-	127	yoshikawa-atsushi$3.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/21 09:49
-	4	yoshikawa-atsushi$3.in. and clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/21 12:57
-	10	yoshikawa-a$10.in. and clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/21 10:02
-	6	(yoshikawa-a$10.in. and clock) not (yoshikawa-atsushi$3.in. and clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/21 10:04
-	487	(716/2).CCLS.	USPAT	2003/07/21 10:04
-	556	(716/6).CCLS.	USPAT	2003/07/21 10:05
-	718	(716/4).CCLS.	USPAT	2003/07/21 10:05
-	1523	((716/2).CCLS.) ((716/6).CCLS.) ((716/4).CCLS.)	USPAT	2003/07/21 10:05
-	796	(((716/2).CCLS.) ((716/6).CCLS.) ((716/4).CCLS.)) and clock$2	USPAT	2003/07/21 10:05
-	372	(((716/2).CCLS.) ((716/6).CCLS.) ((716/4).CCLS.)) and (clock$2 and (flip near2 flop$2))	USPAT	2003/07/21 10:06
-	211	(((716/2).CCLS.) ((716/6).CCLS.) ((716/4).CCLS.)) and (clock$2 and (flip near2 flop$2) and latch$2)	USPAT	2003/07/21 11:14
-	14	716/$.ccls. and (clock$2 same (flip near2 flop$2) same pipeline$2)	USPAT	2003/07/21 11:22
-	10	716/$.ccls. and (latch$2 same (flip near2 flop$2) same pipeline$2)	USPAT	2003/07/21 11:24
-	340	716/$.ccls. and (latch$2 same (flip near2 flop$2))	USPAT	2003/07/21 11:24
-	18	716/$.ccls. and (latch$2 same (flip near2 flop$2) same divid$3)	USPAT	2003/07/21 11:26
-	96	716/$.ccls. and (latch$2 same (flip near2 flop$2) same delay)	USPAT	2003/07/21 11:29
-	31	(716/$.ccls. and (latch$2 same (flip near2 flop$2) same delay)) not ((((716/2).CCLS.) ((716/6).CCLS.) ((716/4).CCLS.)) and (clock$2 and (flip near2 flop$2) and latch$2))	USPAT	2003/07/21 12:54
-	0	6101621.uref.	USPAT	2003/07/21 12:55
-	1	6101621.pn.	USPAT	2003/07/21 12:56
-	1	"5818745".PN.	USPAT	2003/07/21 12:55
-	1	"5784307".PN.	USPAT	2003/07/21 12:55
-	1	"5752061".PN.	USPAT	2003/07/21 12:56
-	1	"5694350".PN.	USPAT	2003/07/21 12:56
-	1	"5568412".PN.	USPAT	2003/07/21 12:56
-	3	yoshikawa-atsushi$3.in. and (flip near3 flop)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/21 12:57
-	6447	nec$.as. and (flip near flop)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/21 13:00

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-	3309	mitsubishi$.as. and (flip near flop)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/21 13:00
-	989	mitsubishi$.as. and ((flip near flop) and latch$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/21 13:01
-	30	mitsubishi$.as. and ((flip near flop) and latch$2 and optimiz$3 and path)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/21 13:03
-	59105	variable with range	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:07
-	1269	variable with range same operator	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:48
-	64	(variable with range same operator) and (circuit with design)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:43
-	7	(variable with range same operator) and (circuit with design) and (716/$.ccls. or 703/$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:39
-	8	(variable with range same operator) and (compile$3) and (716/$.ccls. or 703/$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:39
-	11	((variable with (range or assum$4)) same operator) and (compile$3) and (716/$.ccls. or 703/$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:40
-	8	(((variable with (range or assum$4)) same operator) and (compile$3) and (716/$.ccls. or 703/$.ccls.)) not ((variable with range same operator) and (circuit with design) and (716/$.ccls. or 703/$.ccls.))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:40
-	61	((variable with range same operator) and (circuit with design)) not (((variable with (range or assum$4)) same operator) and (compile$3) and (716/$.ccls. or 703/$.ccls.))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:45
-	83	(variable with range same operator) and compile$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:45
-	76	((variable with range same operator) and compile$3) not (((variable with range same operator) and (circuit with design)) not (((variable with (range or assum$4)) same operator) and (compile$3) and (716/$.ccls. or 703/$.ccls.)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:45

-	1508	(variable with (range or assum$4)) same operator	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:49
-	0	((variable with (range or assum$4)) same operator) and (716/$.did. or 703/$.did.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:49
-	29	((variable with (range or assum$4)) same operator) and (716/$.ccls. or 703/$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:50
-	14	(((variable with (range or assum$4)) same operator) and (716/$.ccls. or 703/$.ccls.)) not ((((variable with (range or assum$4)) same operator) and (compile$3) and (716/$.ccls. or 703/$.ccls.)) ((((variable with (range or assum$4)) same operator) and (compile$3) and (716/$.ccls. or 703/$.ccls.)) not ((variable with range same operator) and (circuit with design) and (716/$.ccls. or 703/$.ccls.))) (((variable with range same operator) and (circuit with design)) not (((variable with (range or assum$4)) same operator) and (compile$3) and (716/$.ccls. or 703/$.ccls.))) ((variable with range same operator) and compile$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:53
-	2408	data with specification with (repesentation or communication)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 10:57
-	5	(data with specification with (repesentation or communication)) and 716/$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 11:05
-	653	carter-R$.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 11:05
-	114	carter-R$.in. and (algorithm or variable or assum$4 or specification)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 11:06
-	0	(carter-R$.in. and (algorithm or variable or assum$4 or specification)) and (716/$.ccls. or 703/$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 11:07
-	123	carter-R$.in. and (algorithm or variable or assum$4 or specification)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 11:08
-	10	carter-R$.in. and (hewlett$.as.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 11:08

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-	7442	hewlett.as. and (algorithm or variable or assum$4 or specification)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 11:09
-	15555	hewlett$.as. and ((algorithm or data or communication) or specification)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 11:09
-	324	hewlett$.as. and ((algorithm or data or communication) with specification)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 11:10
-	9	(hewlett$.as. and ((algorithm or data or communication) with specification)) and 716/$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 11:12
-	33	bach-c$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/30 11:13
-	454	(716/18).CCLS.	USPAT	2003/07/30 11:13
-	0	("27andcompile$3").PN.	USPAT	2003/07/30 11:14
-	0	("27andcompile$3").PN.	USPAT	2003/07/30 11:14
-	0	("27andcompile$3").PN.	USPAT	2003/07/30 11:14
-	0	((716/18).CCLS.) and compile$3 and specfication$3	USPAT	2003/07/30 11:15
-	198	((716/18).CCLS.) and compile$3	USPAT	2003/07/30 12:59
-	4190	carry near2 save or "2's complement" or signed near2 digit or full$2 near2 redundant	USPAT	2003/07/30 13:03
-	1540	(carry near2 save or "2's complement" or signed near2 digit or full$2 near2 redundant) and (serial or lsdfirst or digitsize or full$2 near2 parallel or digit near2 serial or bit near2 serial)	USPAT	2003/07/30 13:06
-	13	((carry near2 save or "2's complement" or signed near2 digit or full$2 near2 redundant) and (serial or lsdfirst or digitsize or full$2 near2 parallel or digit near2 serial or bit near2 serial)) and 716/$.ccls.	USPAT	2003/07/30 14:51

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